

Page 1 (U.S. Patents)

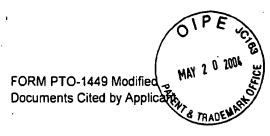
U.S. App. No.: 09/853,925 Filed: May 9, 2001 Group: 2813

Applicant: Chu et al Docket No.: NSWC-1008US

Examiner	Item					
Initial	No.	Document No.	Date	Name Name	Class	Subclass
FX	1US	3,642,526	Feb-72	Itoh, et al.		
1	2US	3,784,402	Jan-74	Reedy, Jr.		
	3US	4,368,098	Jan-83	Manasevit		
	4US	4,765,845	Aug-88	Takada, et al.		
	5US	4,983,535	Jan-91	Blanchard		
	6US	4,996,584	Feb-91	Young, et al.		
	7US	5,036,017	Jul-91	Noda		
	8US	5,084,417	Jan-92	Joshi, et al.	•	
	9US	5,135,808	Aug-92	Kimock, et al.		
· .	10US	5,221,853	Jun-93	Joshi, et al.		
	11US	5,225,561	Jul-93	Kirlin et al.		
	12US	5,232,872	Aug-93	Ohba		
	13US	5,280,012	Jan-94	Kirlin et al.		
	14US	5,352,917		Ohmi		
	15US	5,400,739	Mar-95	Kao et al.		
	16US	5,453,494		Kirlin et al.		
	17US	5,625,204	'Apr-97	Kao et al.		
	18US	5,920,080	Jul-99	Jones		
	19US	5,932,006	Aug-99	Santiago et al.		
	20US	5,955,785	Sep-99	Gardner et al.		
	21US	5,965,810	Oct-99	Holbrook		
	22US	5,968,847	Oct-99	Ye et al.		
	23US	6,077,775	Jun-00	Stumborg et al.		
	24US	6,120,844	Sep-00	Chen et al.		
	25US	6,139,905		Chen et al.		
—	26US	6,144,050	Nov-00	Stumborg et al.		
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EXAMINER:

DATE CONSIDERED:



Page 1 (Foreign)

U.S. App. No.: 09/853,925 Filed: May 9, 2001 Group: 2813

Applicant: Chu et al

Docket No.: NSWC-1008US

Examiner	Item	Foreign Patent			Translation
Initial	No.	Document No.	Date	Country	(Yes/No)
GL.	1F	WO 00/11731	3/1/2000	PCT	
· 8 K	2F	JP 402143531-A	6/1/1990	Japan	
CV	3F	JP 6-164004	6/10/1994	Japan	·
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Pagë 1 (Other Documents)

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List of Patents and Publications and by Applicant
U.S. Patent Trademark

U.S. App. No.: 09/853,925

Filed: May 9, 2001

Group: 2813 Applicant: Chu et al

Docket No.: NSWC-1008US

Examin r	Item	
Initial	No.	Other Documents (Including Author, Title, Date, Location and Pertinent Pages)
		Vogt et al., "Dielectric Barriers for Cu Metallization Systems", Materials for Advance
· Ce	10D	Metallization, MAM '97, pp 51-52, 1998
	100	
Cic.		"Electrically reprogrammable FAMOS structure," IBM Technical Disclosure Bulletin
EL	20D	3/1/73, Vol. 15, Issue 10, pp. 3264-3266
. Ex		Murarka - "Multilevel interconnections for ULSI and GSI era," Materials Science and
	30D	Engineering, R19 (1997), pp. 87-151
		Murarka - "Diffusion Barriers - for Thin Film Metallizations," Defect and Diffusion
ER	400	Forum, Vol. 59 (1998), pp. 99-110
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	400D	
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